ABSTRACT OF THE DISCLOSURE

An electronic control unit for engines or vehicles has multiple CPUs and a single non-volatile memory such as an EEPROM. The CPUs are programmed to directly receive data from the EEPROM. The CPUs may be programmed to generate respective data retrieving commands when the EEPROM is not being accessed for data retrieval. In this instance, the CPUs execute respective system register initialization processing differently from each other after a start of power supply. For instance, the system register initialization processing of a first CPU is divided while the system register initialization processing of a second CPU is not divided. Alternatively, the CPUs may be programmed to generate a data retrieving command only from the first CPU and receive the retrieved data by both the first CPU and the second CPU at the same time.

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